

**METHOD AND APPARATUS FOR REDUCING CHARGE INJECTION IN A
FET SWITCH**

ABSTRACT OF THE DISCLOSURE

5 A method and apparatus for reducing charge injection in a FET switch. The switch includes a switch FET and two compensating FETs coupled to an input node. Gate drive signals for the two compensating FETs are generated by a gate drive circuit dependent upon the analog input signal and gate drive signal to the switch FET.